

Junior Logic Design Engineer

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Company: microTECH Global Ltd

Location: Netherlands

Category: computer-and-mathematical

Job Title: Digital Design Engineer

Position: Full time, Permanent

Location: The Netherlands, NE Vught

Salary Range: 40, to 50, EUR per annum

Client Information:

Seeking to hire Junior Logic Design Engineers to be a part of the Silicon IP business unit.

You will report to the local R&D Team Lead.

You will develop premier chips and silicon IP providers to make data faster and safer.

Responsibilities:

- Design and Implementation of secure silicon IPs
- Implementation: RTL design, lint, synthesis, IP release
- Support Verification: execute basic verification and assist verification team to complete the IP verification
- Prepare product documentation and Participate in technical reviews

Requirements:

- Bachelor or Master degree in Hardware Engineering, Computing Science or comparable
- Good level of English is required

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